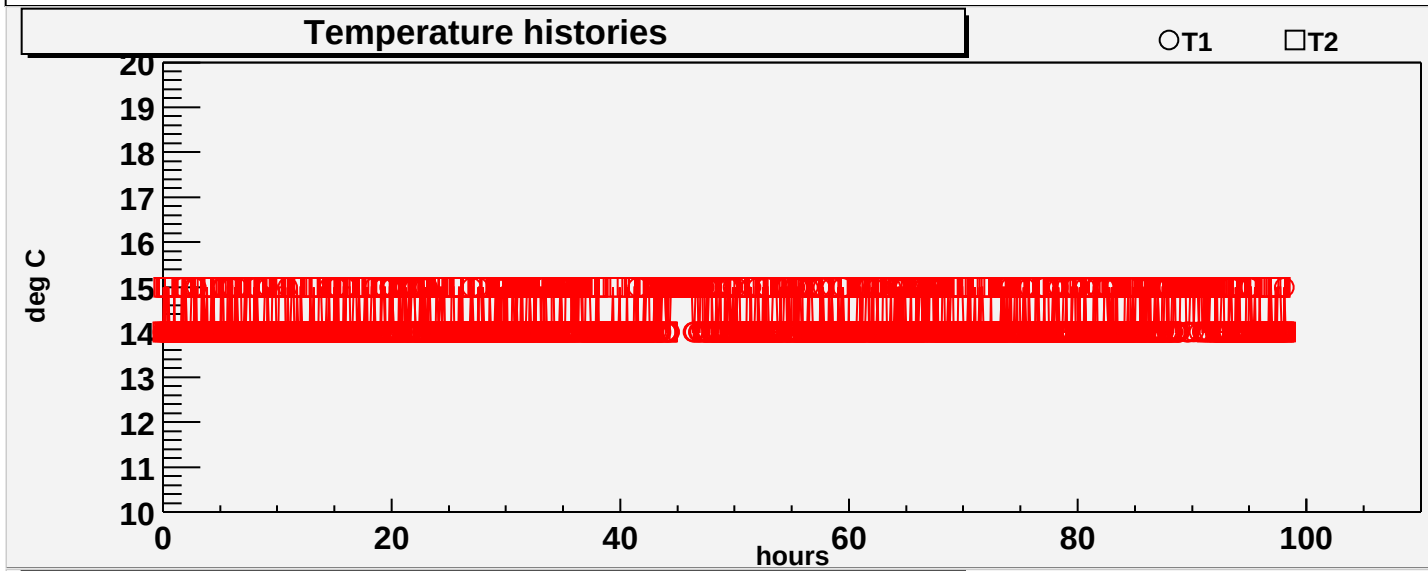
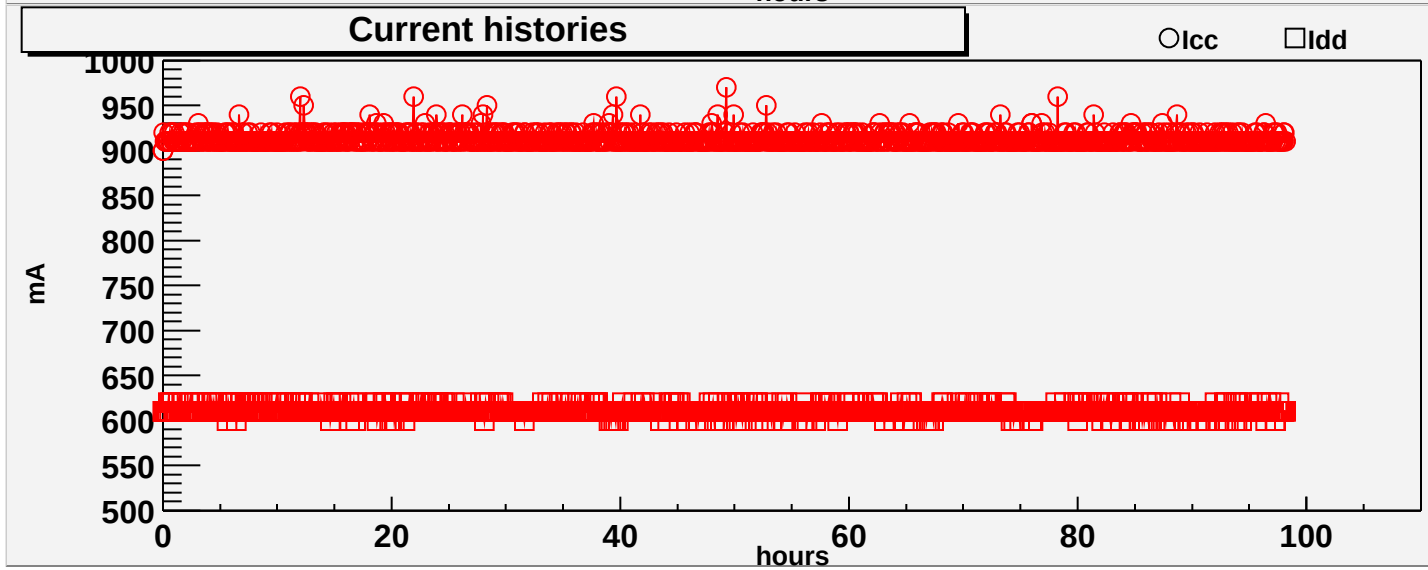
Test started at 05/05/2002 at 21:28:31
last update was 09/05/2002 at 23:38:34
expected end at 10/05/2002 at 01:28:31




Present values:

| Module | T1/C | T2/C |
|---|---|---|
| 0 | 14.00 | 14.00 |




Present values:

| Module | Icc(mA) | Idd(mA) |
|---|---|---|
| 0 | 910 | 610 |